

PATENT Intel/16406

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ju) I hereby certify that this paper is
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Filed: June 23, 2003) mail in an envelope addressed to:
) Commissioner for Patents, P.O.
Assignee: Intel Corporation) Box 1450, Alexandria, VA 22313
<u>-</u>) 1450 on this date:
For: Methods And Apparatus For)
Preserving Precise Exceptions In Code	October 21, 2003
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Group Art Unit: 2183) James A. Flight
)// Registration No. 37,622
Examiner: Unknown	Attorney for Applicant

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INFORMATION DISCLOSURE STATEMENT

Technology Center 2100

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The publications listed on the enclosed PTO Form-1449 are submitted pursuant to

37 CFR §§ 1.56, 1.97, and 1.98. Copies of the publications are enclosed.

TIME OF FILING

This information disclosure statement is being filed, to the best of the undersigned's knowledge, before the mailing date of a first Office action on the merits. In accordance with 37 CFR §1.97(b), no certification or fee is required.

METHOD OF PAYMENT

No fee is required.

The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

Correspondence Address:

Respectfully submitted,

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October 21, 2003

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